



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein  
Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE  
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD  
THERFOR

Application No.: 09/990,894 Filed: November 16, 2001  
Examiner: Connie C. Yoha Group Art Unit: 2818  
Atty. Docket No.: 023-0013 Conf. No.: 3451

May 24, 2004

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**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Dear Sir:

Applicant appreciates the Examiner's reasons for allowance. Applicant respectfully submits that the claims are allowable because the art of record fails to teach, alone or in combination, the invention described in at least each of the independent claims.

<b><u>CERTIFICATE OF MAILING OR TRANSMISSION</u></b>	
I hereby certify that, on the date shown below, this correspondence is being	
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_____ Andrew C. Graham	_____ Date

Respectfully submitted,

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